

## 4.0 Module Pin-Out

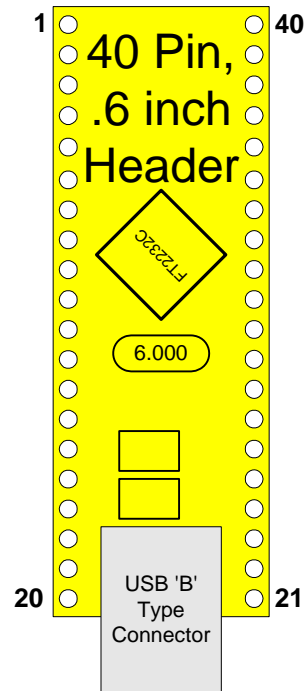


Figure 2. Pin-Out (40 Pin DIP Header )

### 4.1 Pin Definitions

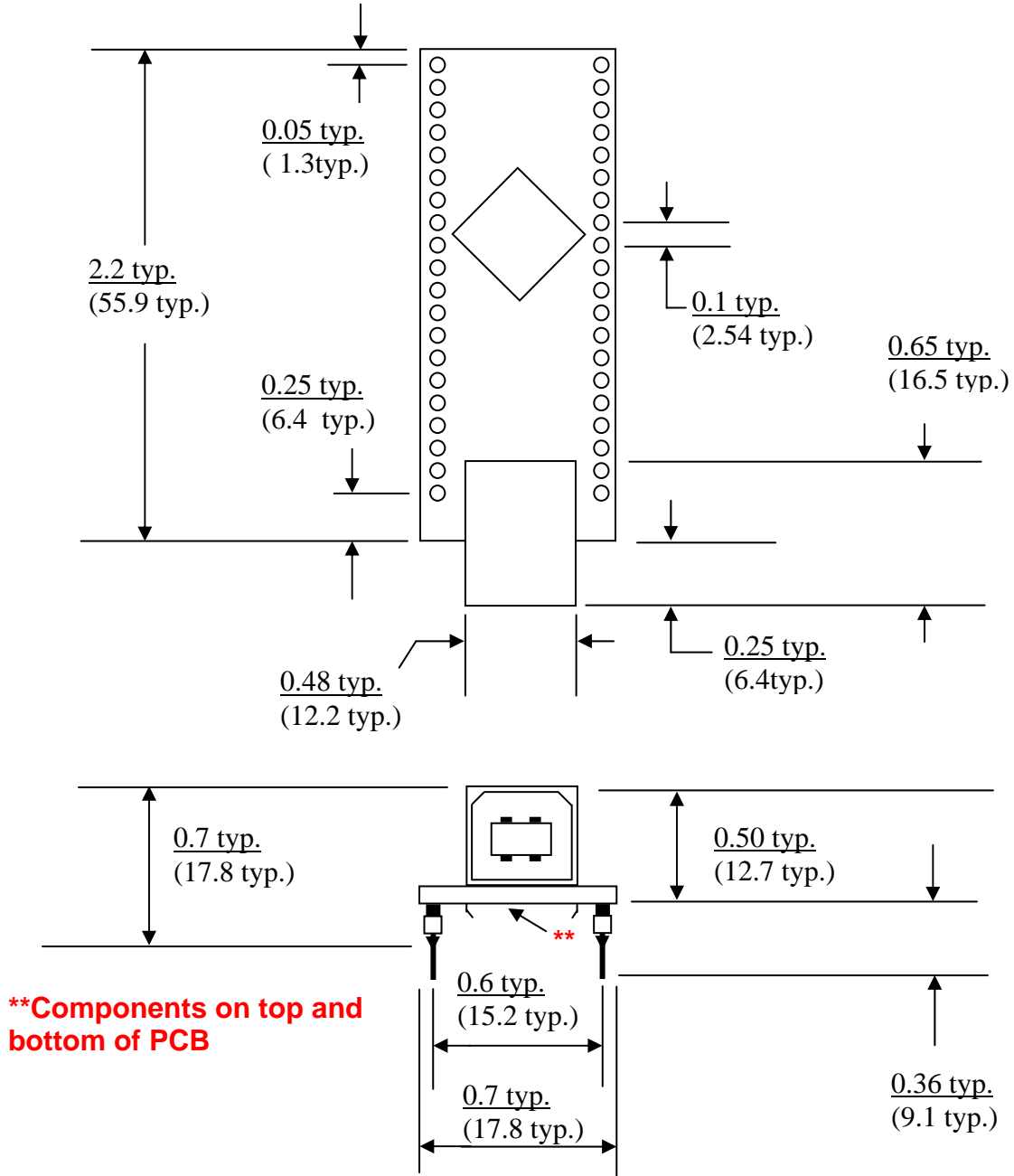
This section describes the operation of the DLP-2232M-G pins. Common pins are defined in the first section and the I/O pins are defined by chip mode. More detailed descriptions of the operation of the I/O pins are provided in section x. (was 9)

### 4.2 Common Pins

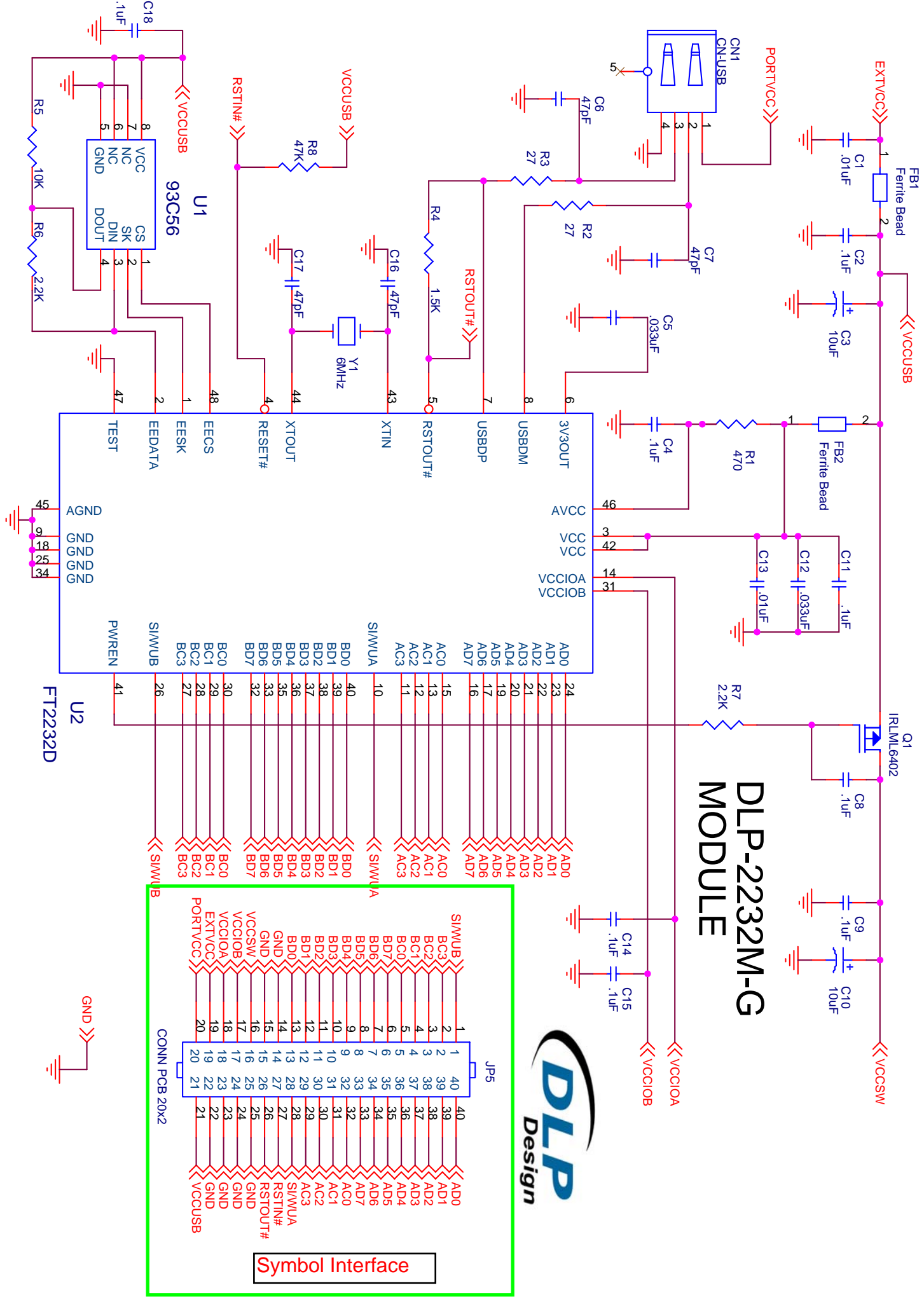
The operation of the following DLP-2232M-G pins stay the same, regardless of the operating mode.

Pin#	Signal	Type	Description
27	RSTIN#	Input	Can be used by an external device to reset the FT2232D. If not required, can be left disconnected.
26	RSTOUT#	Output	Output of the internal Reset Generator. Stays high impedance for ~5ms after VCC > 3.5V and the internal clock starts up, then clamps it's output to the 3.3V output of the internal regulator. Taking RESET# low will also force RSTOUT# to drive low. RSTOUT# is NOT affected by a USB Bus Reset.
19	EXTVCC	PWR	+4.35 to +5.25 volt VCC to the device core, LDO and non-

## 5.0 Mechanical Dimensions Inches (mm)



# DLP-2232M-G MODULE



Symbol Interface