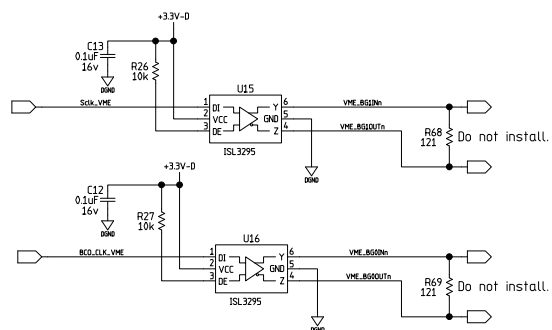


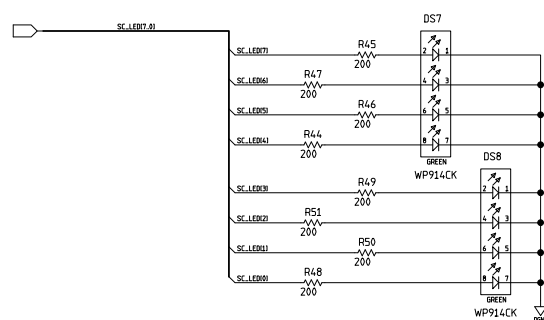
REVISIONS						
ZONE	REV	DESCRIPTION	CHANGED BY	DATE	CLASS REVIEW	APPROVED
	A	ORIGINAL ISSUE			NR	

ZONE		REV	DESCRIPTION	CHANGED BY	DATE	CLASS REVIEW	APPROVED
	A		ORIGINAL ISSUE			NR	

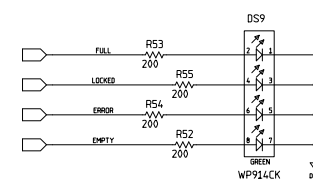
DIFFERENTIAL CONVERSION



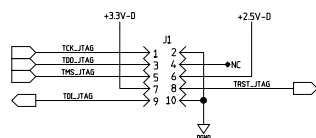
FRONT PANEL LEDS



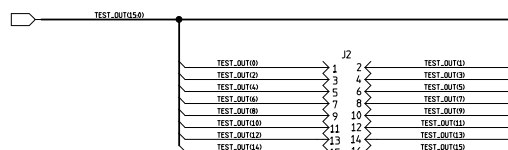
PN WP914CK/4GDT



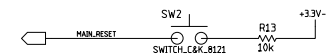
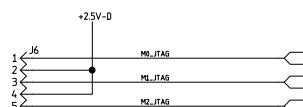
FRONT PANEL JTAG HEADER



TEST OUTPUT HEADER

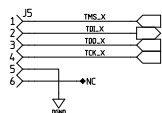
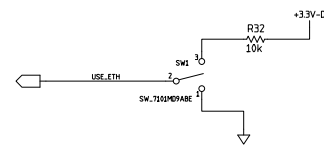


RESET SWITCH

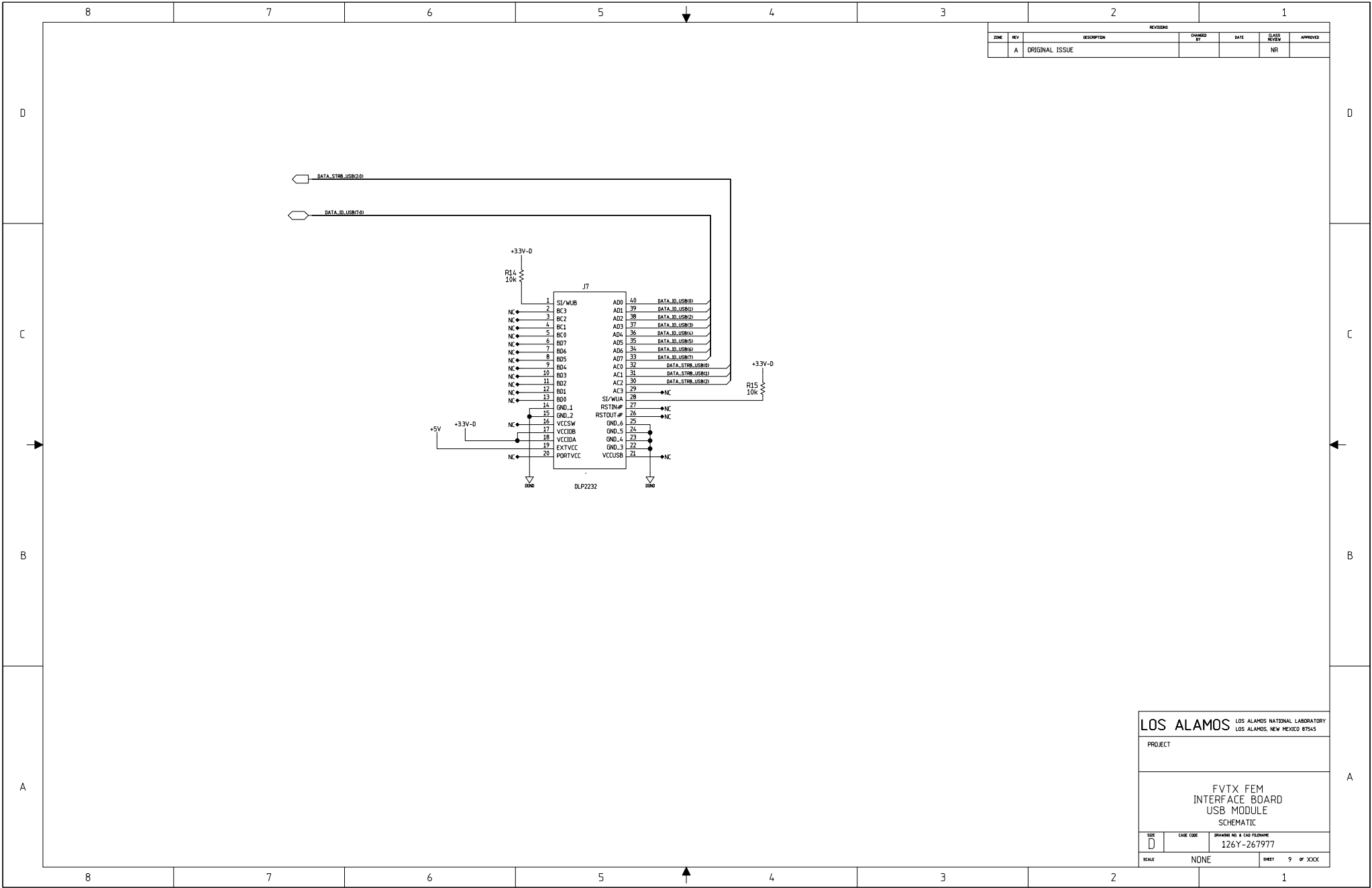
FPGA CONFIGURATION
MODE HEADER

CONFIGURATION MODES
M(2:0) = 0 => master serial
M(2:0) = 5 => JTAG
(Need 0.1" spacing jumpers)

USB/ETHERNET SWITCH



LOS ALAMOS		LOS ALAMOS NATIONAL LABORATORY LOS ALAMOS, NEW MEXICO 87545	
PROJECT			
FVTX FEM INTERFACE BOARD CLOCK DIFFERENTIAL CONVERTERS, FRONT PANEL CONNECTIONS SCHEMATIC			
SIZE D	CASE CODE	DRAWING NO. & CASE NUMBER 126Y-267977	
SCALE NONE		SHEET 8 OF XXXX	



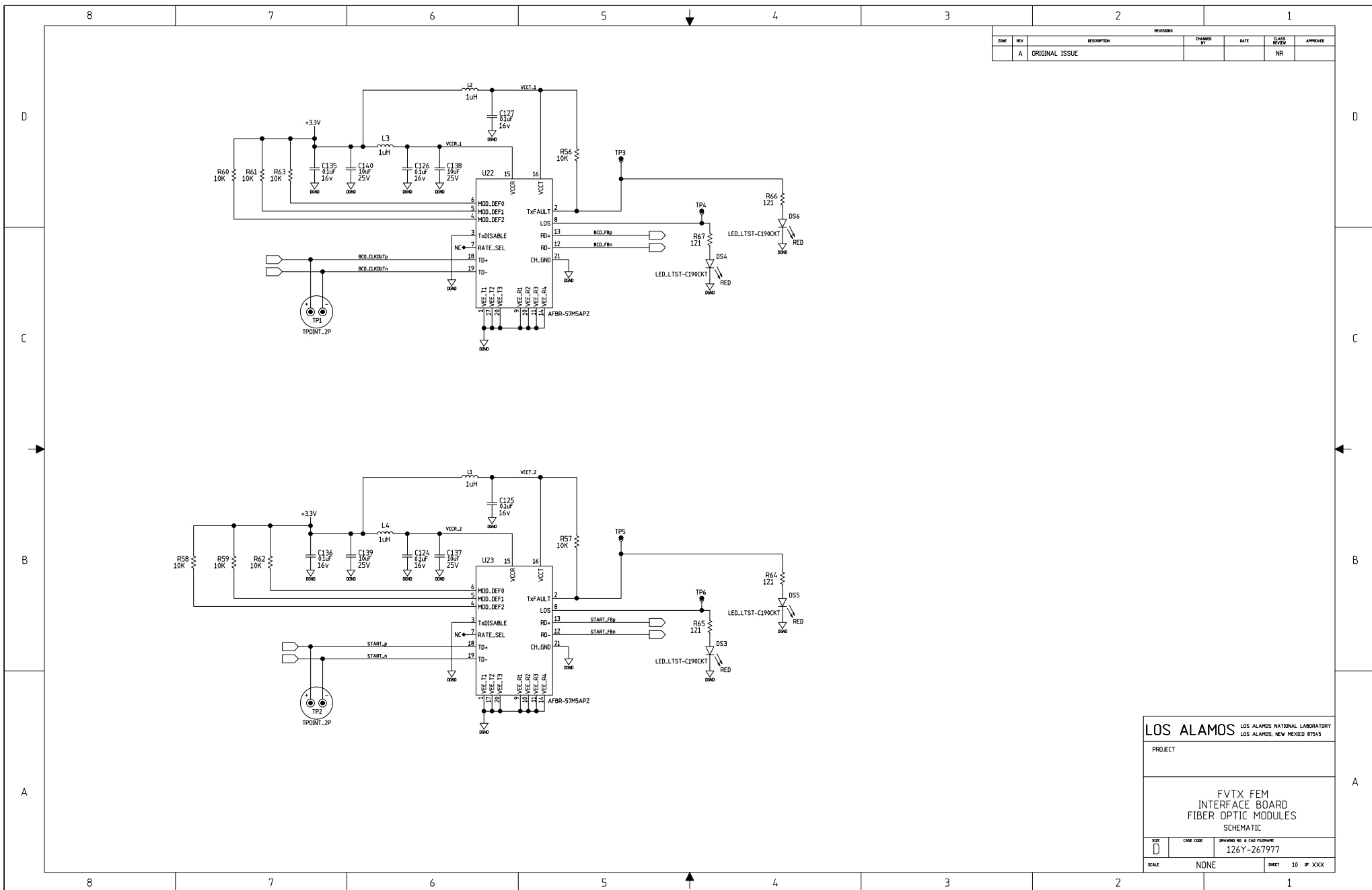
REVISIONS						
ZONE	REV	DESCRIPTION	CHANGED BY	DATE	CLASS REVIEW	APPROVED
	A	ORIGINAL ISSUE			NR	

LOS ALAMOS LOS ALAMOS NATIONAL LABORATORY
LOS ALAMOS, NEW MEXICO 87545

PROJECT

FVTX FEM
INTERFACE BOARD
USB MODULE
SCHEMATIC

SIZE D	CASE CODE	DRAWING NO. & CASE FILENAME 126Y-267977
SCALE NONE	SHEET 9	OF XX



REVISIONS						
ZONE	REV	DESCRIPTION	CHANGED BY	DATE	CLASS REVIEW	APPROVED
	A	ORIGINAL ISSUE			NR	

LOS ALAMOS LOS ALAMOS NATIONAL LABORATORY
LOS ALAMOS, NEW MEXICO 87545

PROJECT

FVTX FEM
INTERFACE BOARD
FIBER OPTIC MODULES
SCHEMATIC

SIZE D CASE CODE DRAWING NO. & CAD FILENAME
126Y-267977

SCALE NONE SHEET 10 OF XXXX