



Silicon Bias Connector 1
 T&B 311-050D072E .05" x .1"
 Latch/Eject, 4 wall, right angle header

60 Contact Header to LeCroy PS
 3M 3372-5302 .1" x .1"
 Latch/Eject, 4 wall,
 right angle header

Silicon Bias Connector 2
 T&B 311-040D072E .05" x .1"
 Latch/Eject, 4 wall, right angle header

Resistors in 14 pin DIP packages, installed in sockets.

Los Alamos National Laboratory	
Title:	MVD HV Dist. VME Module
Size	Date Originated: 3 May 1999
	Last Revised: 19 May 1999
LANL DWG #:	MVD HV DIST
PHENIX #: 002-0301-102	Approved By: Boissevain
Filename:	MVD HV DIST.sch
	Drawn by: G. Hart P-25
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